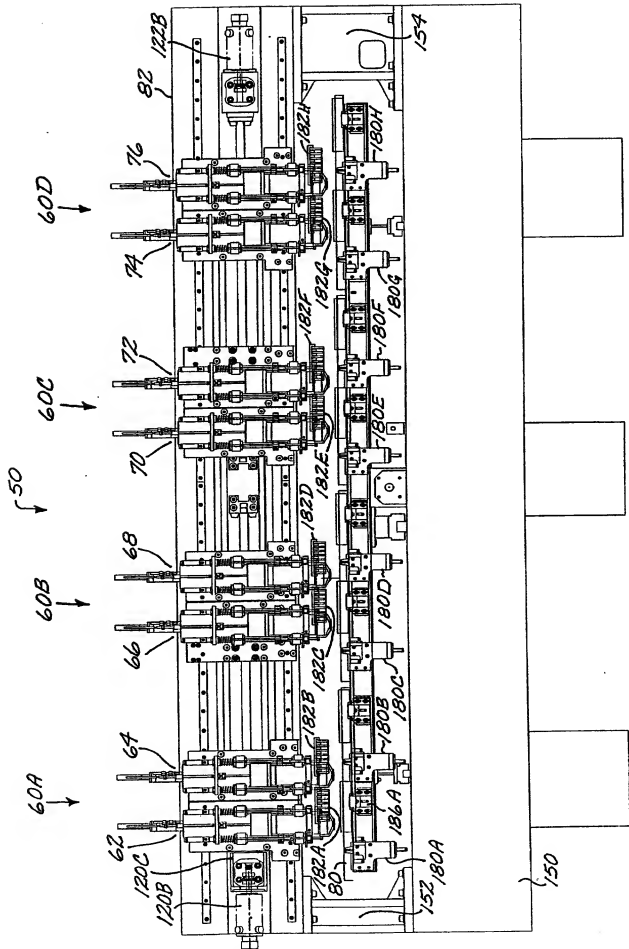


FIG. 1





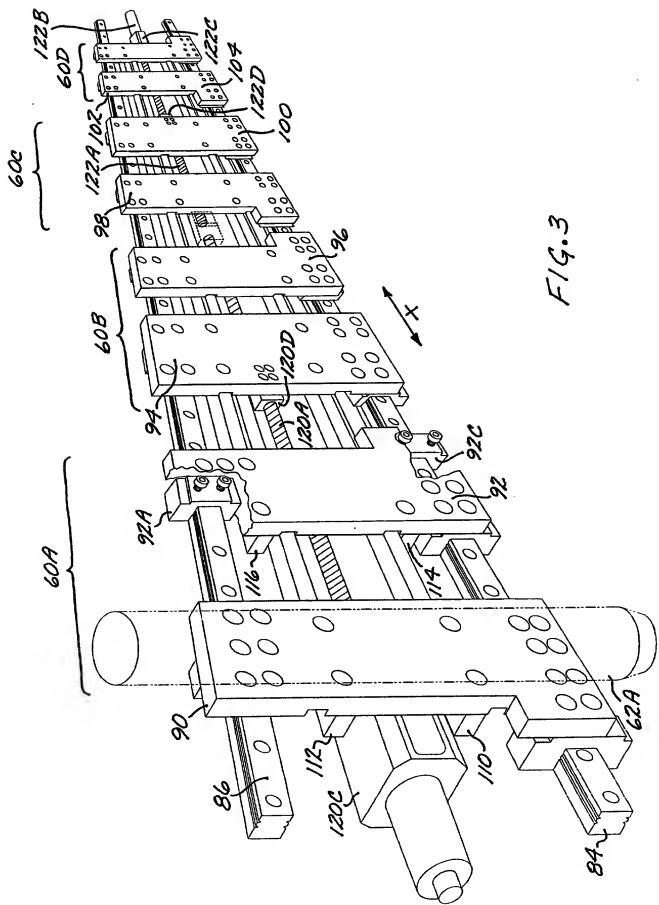
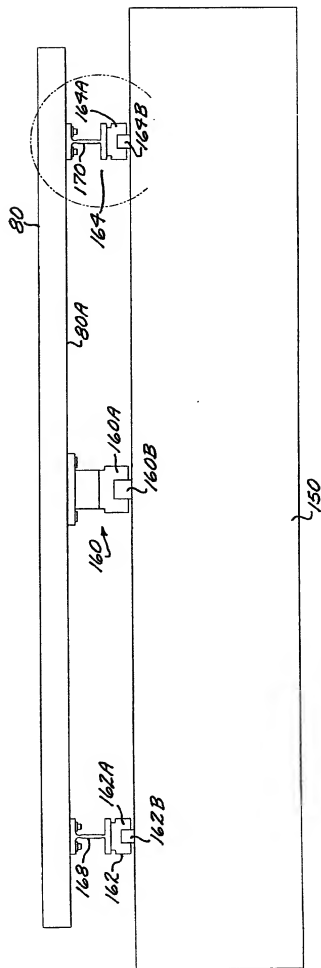


FIG. 3

FIG. 4



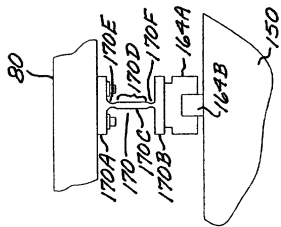


FIG. 5

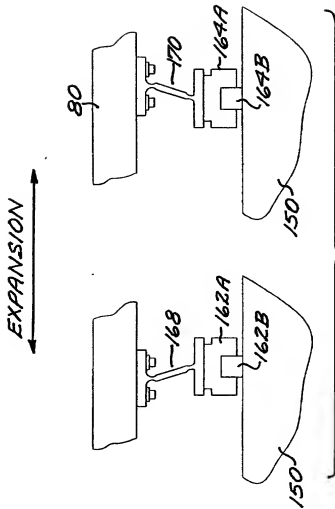


FIG. 6



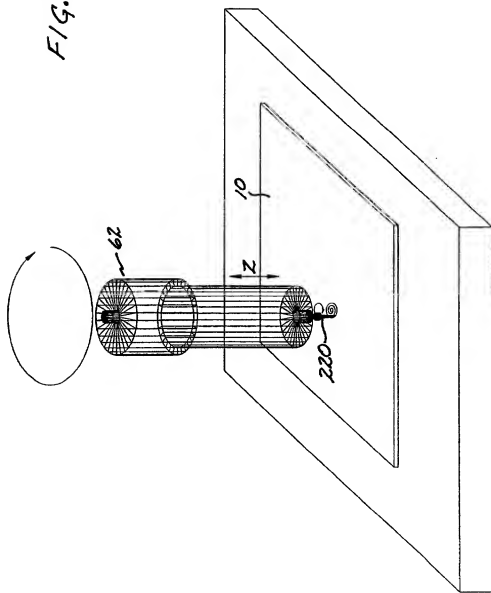
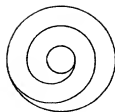


FIG. 10



ROUTER PATH THROUGH THE  
MATERIAL TO EVACUATE HOLE  
VOLUME IN CHIP FORM

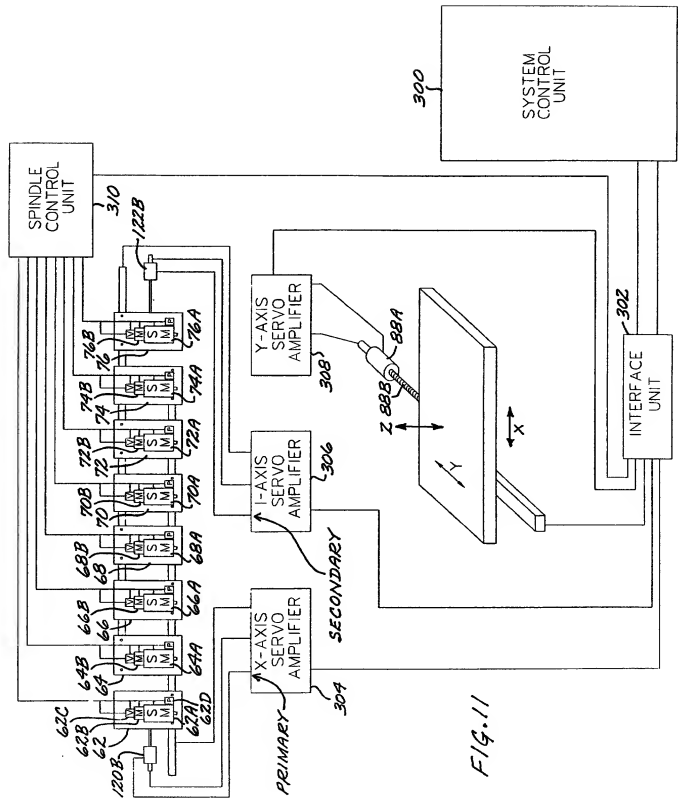


FIG. 11



60A

SP.2

62

SP.1

64

FIG. 12

12

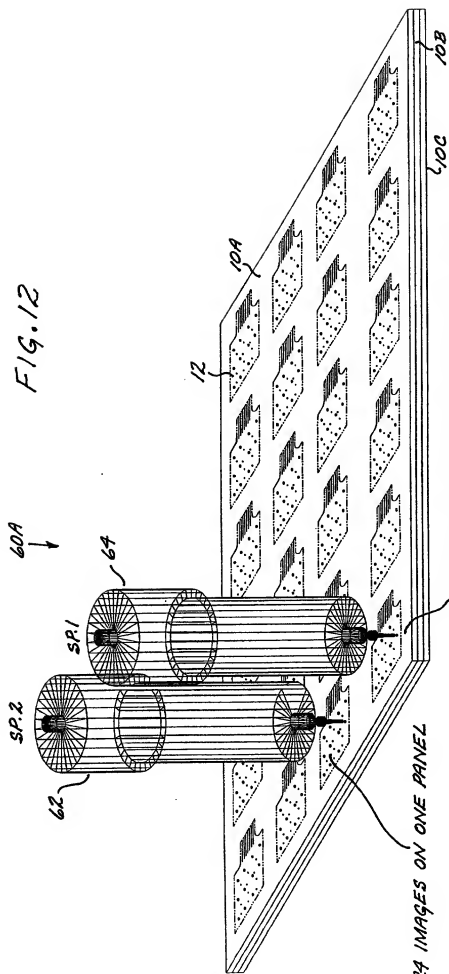
10A

24 IMAGES ON ONE PANEL

DRILLED PANEL PROCESSED WITH  
24 STEP AND REPEAT IMAGES

10C

10B



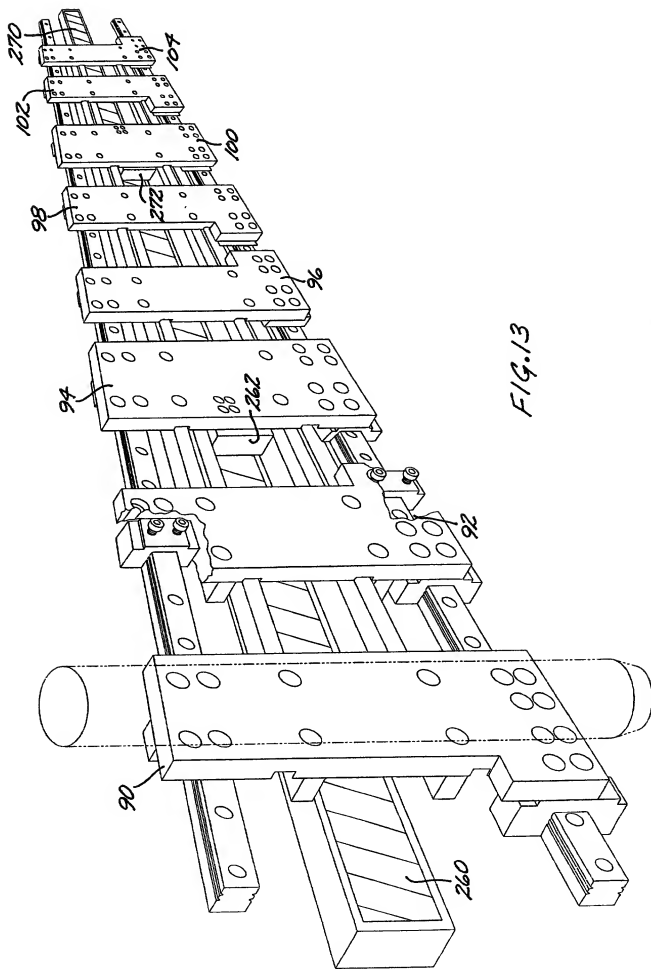


FIG. 13